

async 2010

(co-located with NOCS 2010)

16th IEEE* International Symposium on
Asynchronous Circuits and Systems

call for papers

The International Symposium on Asynchronous Circuits and Systems is the premier forum for researchers to present their latest findings in the area of asynchronous design. Authors are invited to submit full papers on any aspect of asynchronous design, ranging from the core topics of design, synthesis and test, to asynchronous applications in system-level integration and emerging computing technologies. Topics of interest include, but are not limited to:

- Asynchronous/mixed-timed circuits, architectures, memories and interfaces, including interfaces with analogue and mixed-signal domains
- Design models and methods for asynchronous buses, networks on chip (NoC), system-on-chip (SoC) and multi-chip interconnects
- Asynchronous power-adaptive computing, ultra-low power systems, electronics for energy harvesting
- Asynchrony in emerging technologies, including genetic, neural, nano and quantum computing
- Embedded system design with asynchronous architectures/implementations
- Elastic and latency-tolerant synchronous design and GALS systems
- CAD tools for asynchronous design, synthesis, analysis and optimization
- Synchronization, arbitration, metastability modeling and analysis
- Physical design of asynchronous logic and pipelines
- Formal methods for correctness, and performance/power analysis
- Test, reliability, security, and radiation tolerance
- Asynchronous variability-tolerant design and design for manufacturing
- Motivating case studies, comparisons, and applications

Papers should not exceed ten pages in IEEE double-column conference format (single-spaced, 10pt or larger font size), including figures and bibliography. Abstracts of up to 150 words must be included, and papers should be submitted via the paper submission web site (to be opened Sep. 2009). Papers will be evaluated by the program committee on the basis of scientific merit, innovation, relevance, and presentation. New-idea papers are encouraged, and the program committee recognizes that such papers may contain different evaluation criteria based on the lower maturity of novel technology than papers in established areas. Accepted papers will be published in an IEEE conference proceedings.

Important Dates:

Abstract registration:	November 30, 2009
Full paper submission:	December 7, 2009
Notification of acceptance:	February 1, 2010
Final version due:	March 1, 2010

*Note: ASYNC is an IEEE symposium; sponsorship still pending for 2010

Grenoble,
France

3rd-6th May 2010

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